



Attorney Docket No: NVID001/00US

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Priem
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Priem *et al.*

Serial No.: 09/056,656

Examiner: U. Chauhan

Filed: April 7, 1998

Art Unit: 2671

For: **Texture Cache For A Computer Graphics Accelerator**

Assistant Commissioner for Patents
Washington, D.C. 20231

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PRELIMINARY AMENDMENT

Prior to examination of the above-identified application, the Examiner is respectfully requested to enter the following amendments.

IN THE CLAIMS

Please amend claims 42, 52, 56, 62, 66, and 69 as follows:

42. (Amended) A computer system, comprising:

a bus;

a central processing unit coupled to said bus; and

a graphics accelerator coupled to said bus, said graphics accelerator including a texture cache system, said texture cache system including a texture cache memory that stores texels to be used by a texel value generating circuit, a cache controller that controls

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